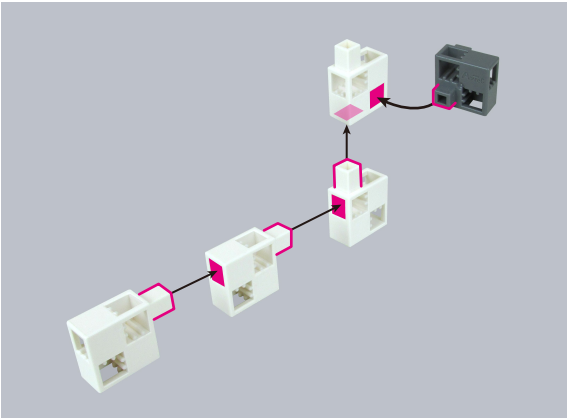
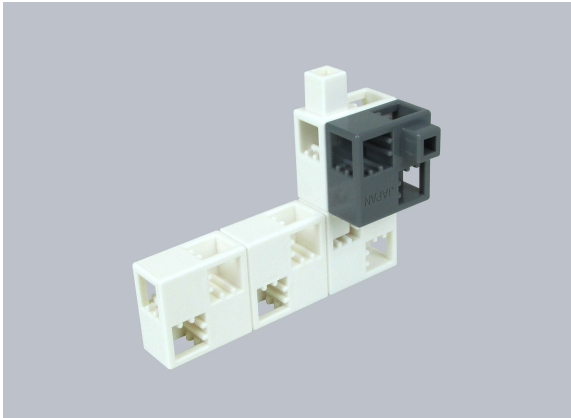


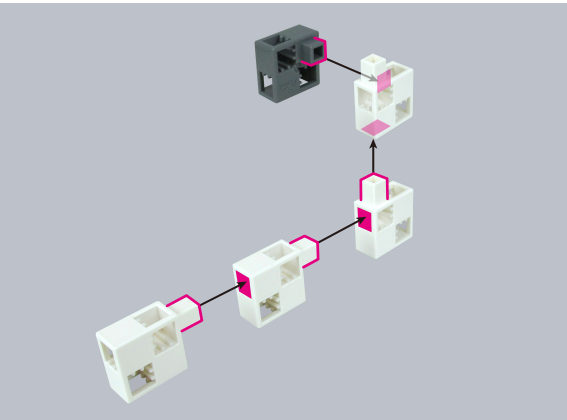
デジタル時計の組み立て

①

An exploded view diagram for step 1. It shows a horizontal base of three white 1x3 Technic bricks. A vertical stack of two white 1x2 bricks is positioned above the middle brick. A black 1x2 digital display module is shown to the right, with a pink line indicating it will be attached to the top of the vertical stack.

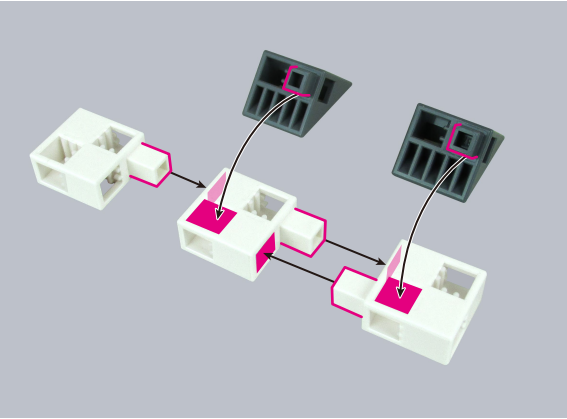
The assembled result of step 1: a horizontal base of three white 1x3 bricks with a vertical stack of two white 1x2 bricks on top of the middle one, and a black 1x2 digital display module attached to the top of that stack.

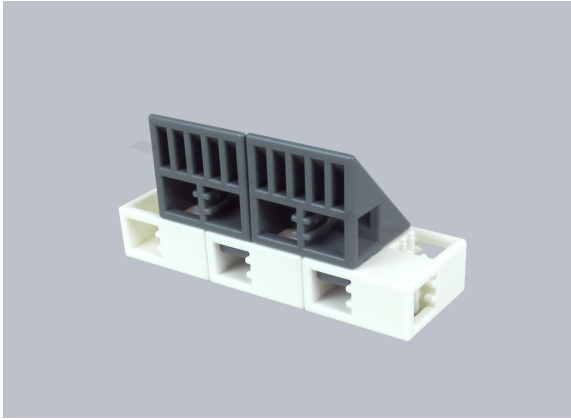
②

An exploded view diagram for step 2. It shows the assembly from step 1. A black 1x2 digital display module is shown above the vertical stack, with a pink line indicating it will be attached to the side of the top white 1x2 brick.

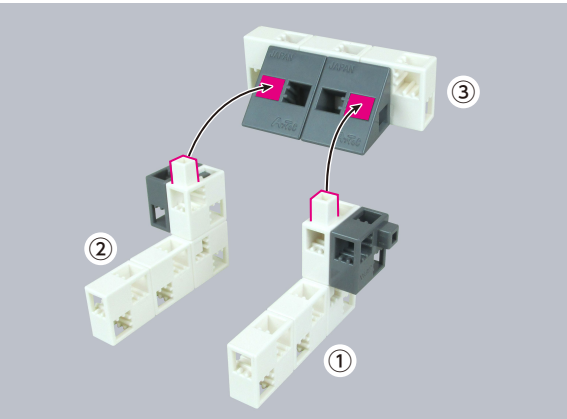
The assembled result of step 2: the black 1x2 digital display module is now attached to the side of the top white 1x2 brick, facing away from the main body.

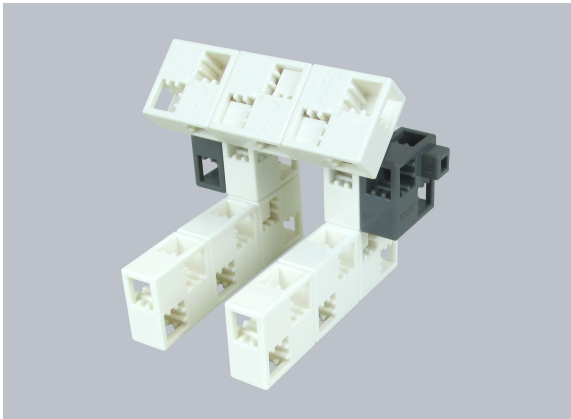
③

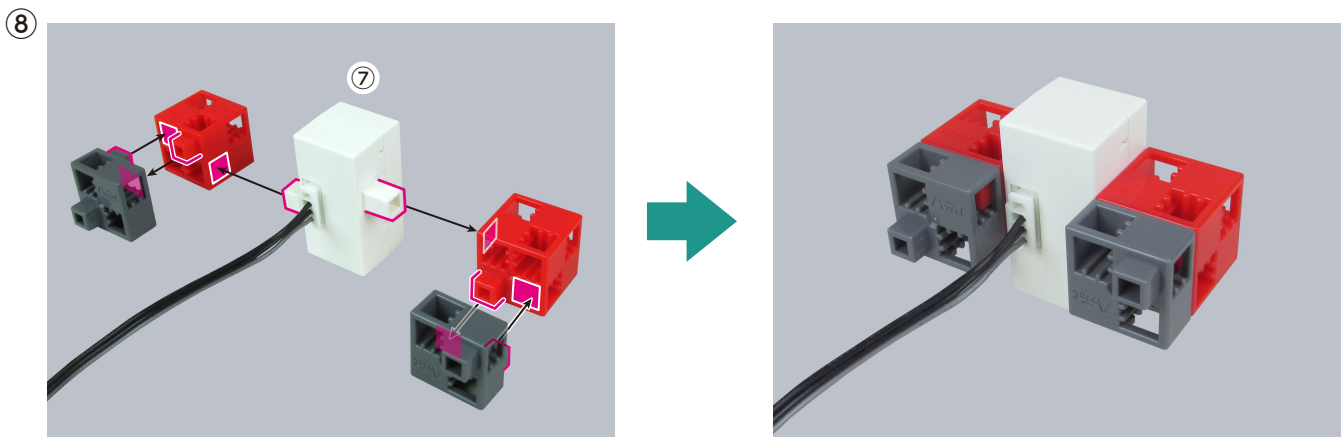
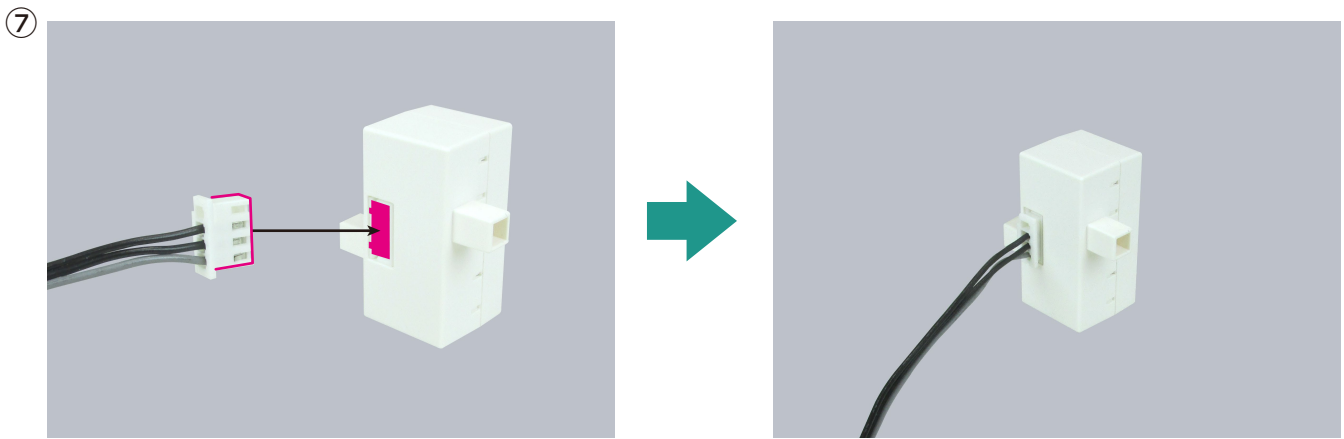
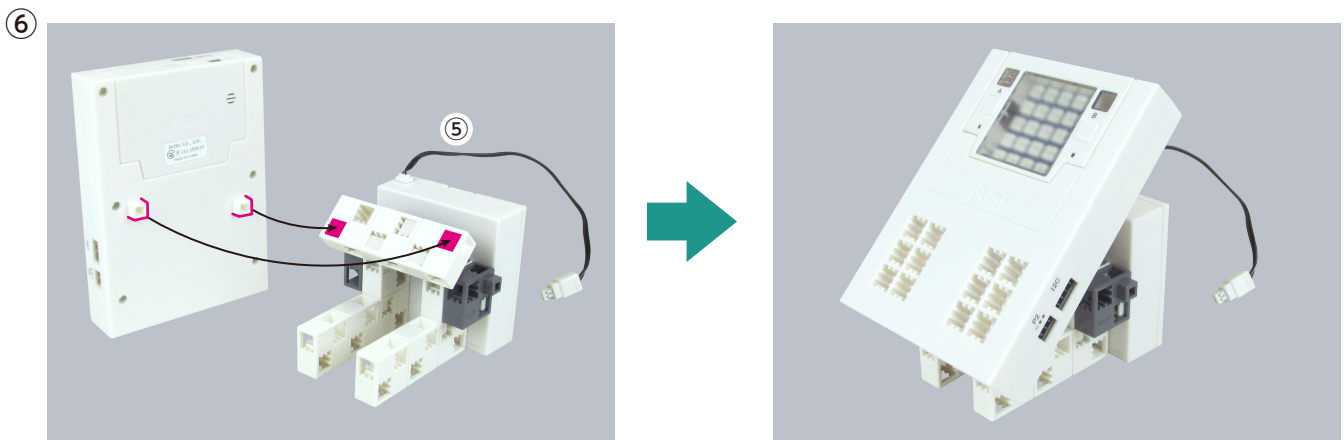
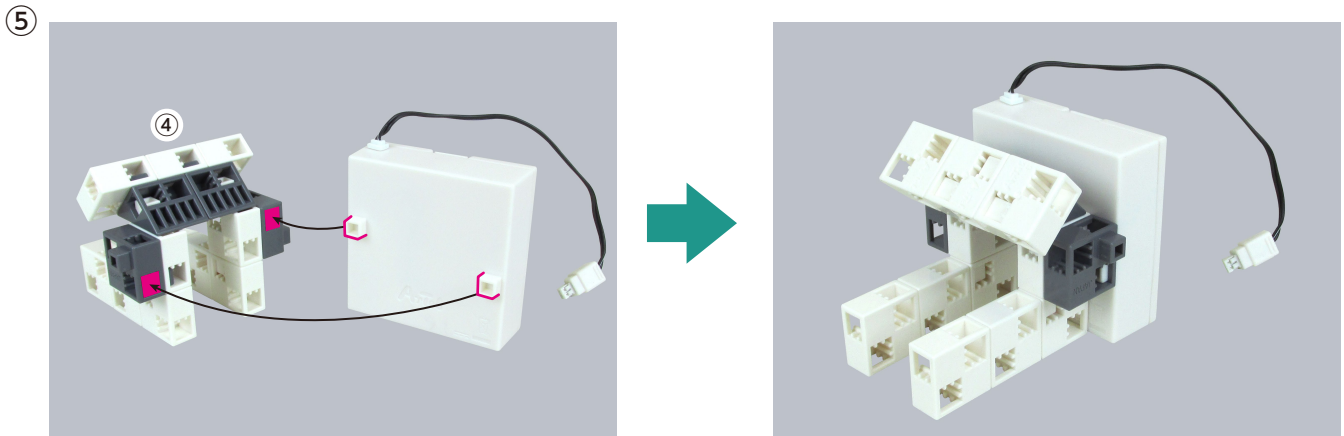
An exploded view diagram for step 3. It shows the assembly from step 2. Two black 1x2 digital display modules are shown above the horizontal base, with pink lines indicating they will be attached to the top of the two outer white 1x3 bricks.

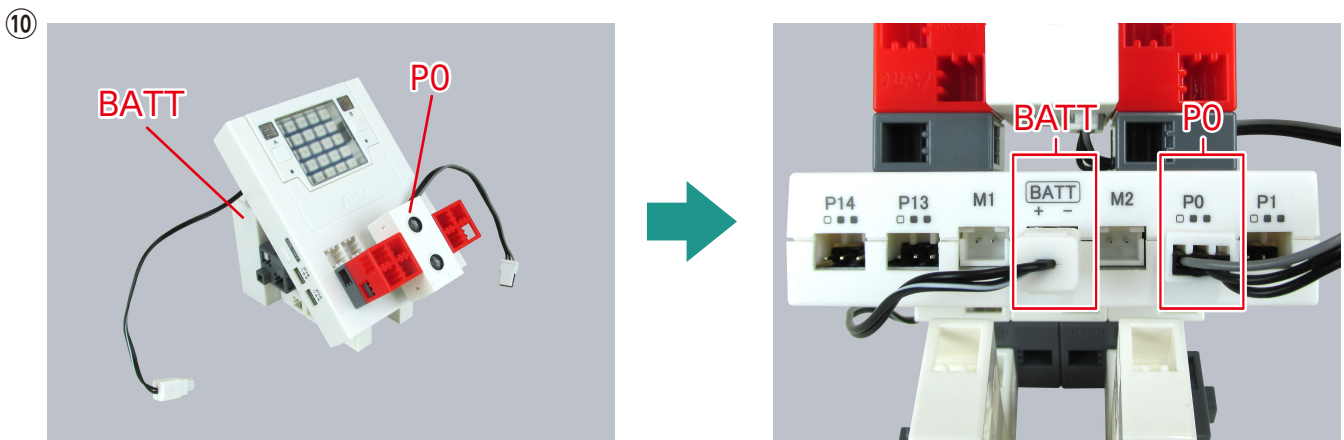
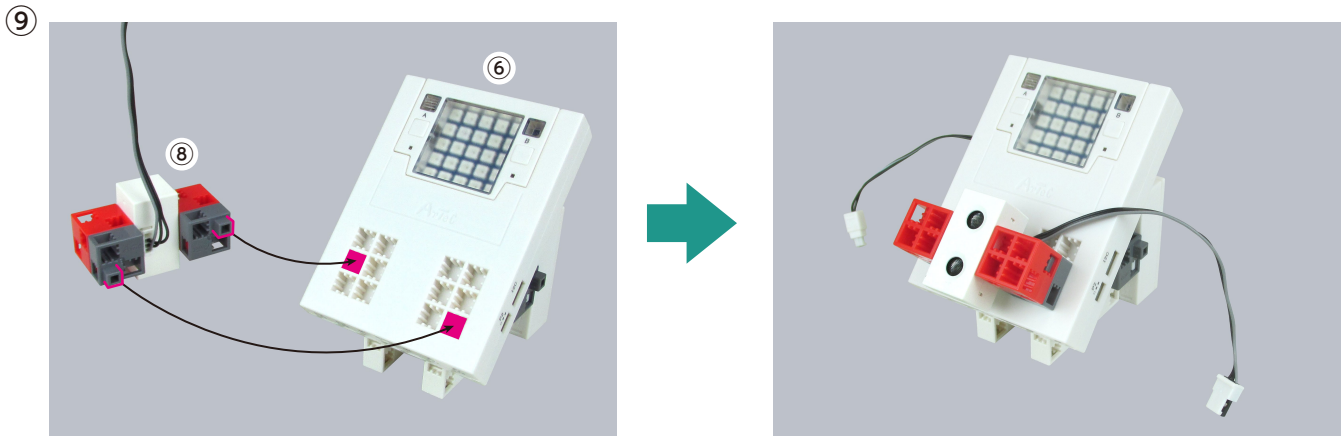
The assembled result of step 3: two black 1x2 digital display modules are attached to the top of the two outer white 1x3 bricks, facing each other.

④

An exploded view diagram for step 4. It shows the assembly from step 3. A white 1x3 Technic brick is shown above the two black display modules, with pink lines indicating it will be attached to the top of both. The sub-assemblies from steps 1 and 2 are labeled with circled numbers 1 and 2 respectively. A circled number 3 is next to the white 1x3 brick.

The assembled result of step 4: the white 1x3 brick is attached to the top of both black display modules, completing the digital clock structure.





完成